



NOTICE: VACANCY FOR RESEARCH ASSISTANT

Multidisciplinary Research Grant Scheme 2025

(Topic: Improving the Quality and Readability of Sri Lankan Stone Inscriptions for the Readability of Epigraphy)

Minimum Qualifications: Four-year Bachelor Degree with a second class upper in either Electrical/Electronic Engineering, or Computer Engineering or Computer Science. The applicant should also demonstrate a keen interest in Deep Learning Architectures and Image Analysis Concepts, and experience in research and publications.

Salary: Rs. 60,000 per month

Duration: 12 Months (Possibly leading to an MPhil degree)

Project Scope: This research focuses on designing a novel Generative Adversarial Network (GAN) architecture to directly address the significant challenges of data scarcity and image degradation prevalent in Sri Lankan stone inscriptions. The aim is to significantly enhance their readability for epigraphic study. The key deliverables are new, optimized GAN model and a task-specific dataset from local inscriptions.

Application Procedure: The CV and degree certificate, along with a cover letter indicating work experience, shall be sent by registered post to the address, Prof. Maheshi Dissanayake, Department of Electrical and Electronic Engineering, Faculty of Engineering, University of Peradeniya, Sri Lanka or by email to maheshid@eng.pdn.ac.lk on or before 12.00 noon on 14-06-2025. Short-listed candidates will be called for an interview on 17-06-2025. The successful applicant should be prepared to commence work on 23-06-2025.

29th May, 2025